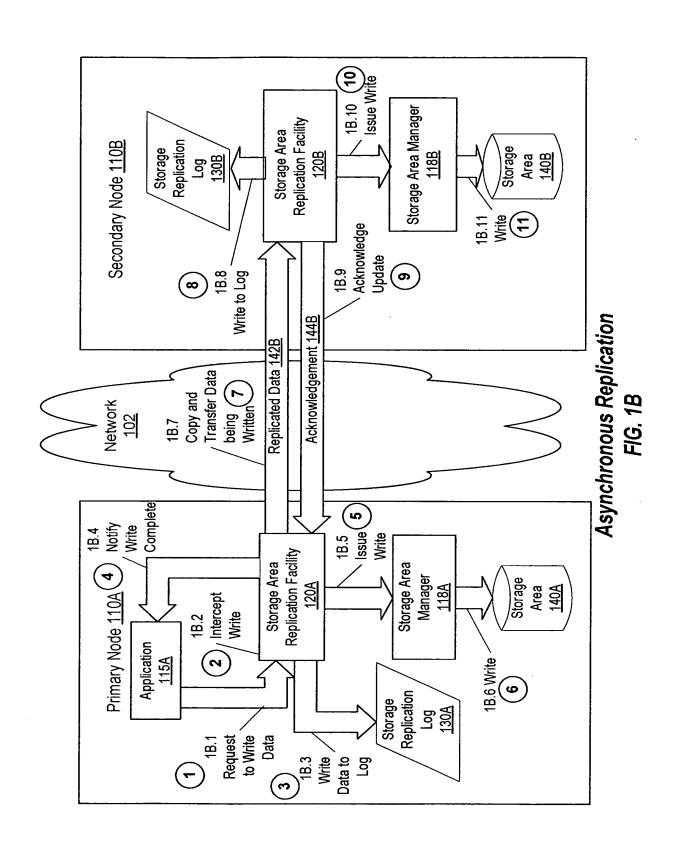
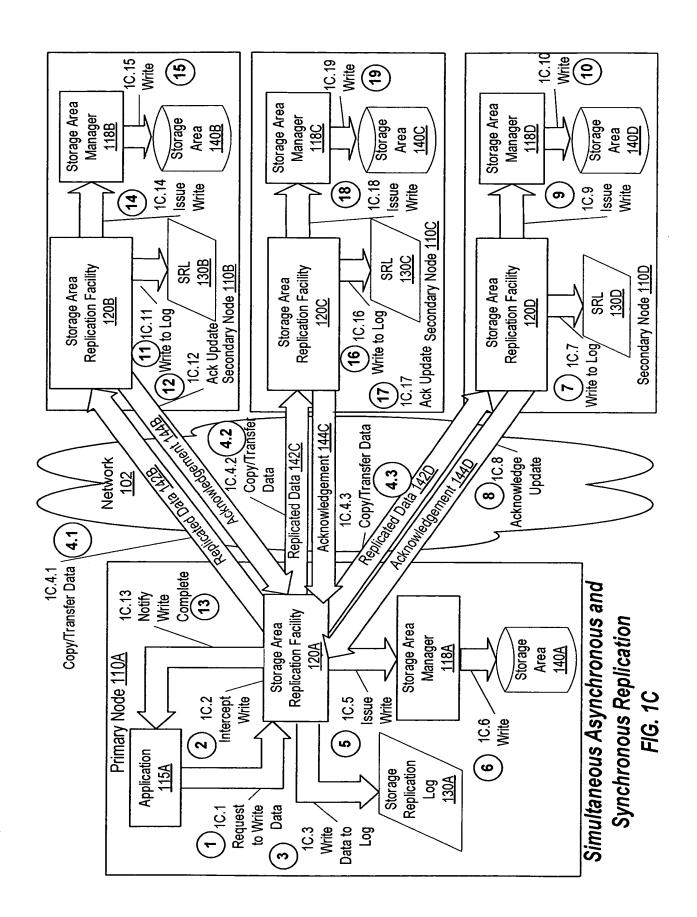


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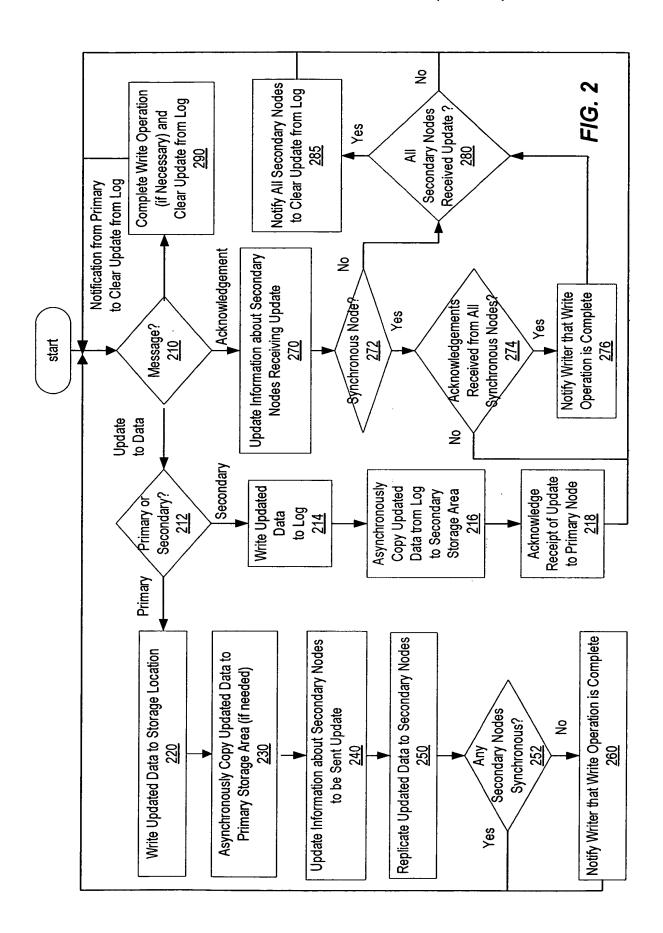
Fast Failover With Multiple Secondary Nodes

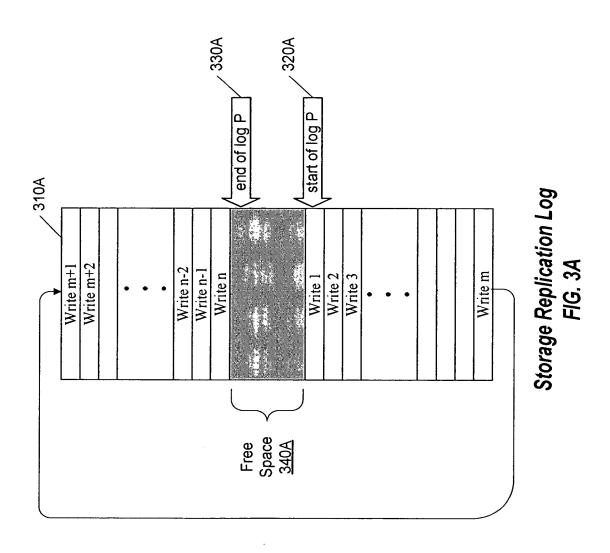




VRT0090P1US Angshuman Bezbaruah

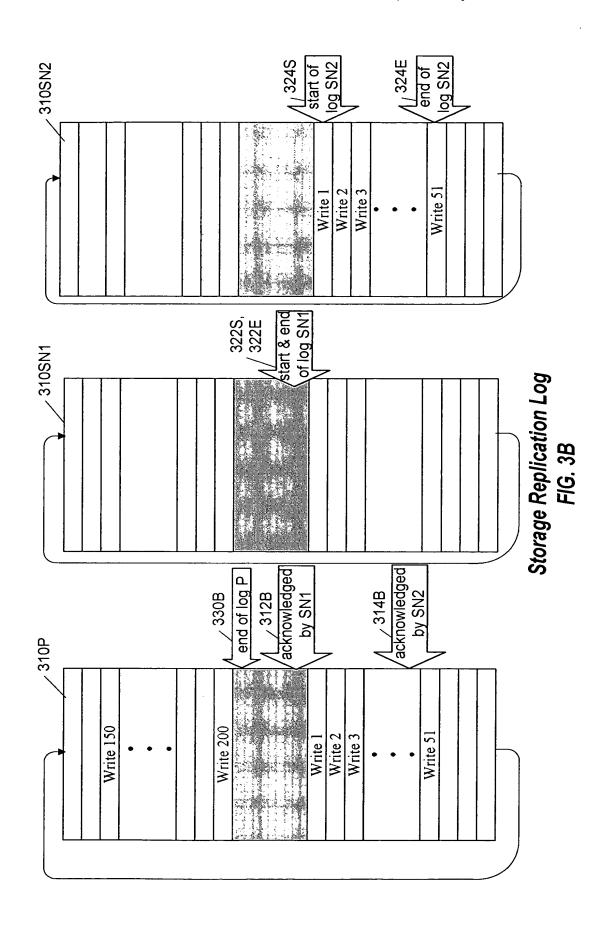
Fast Failover With Multiple Secondary Nodes

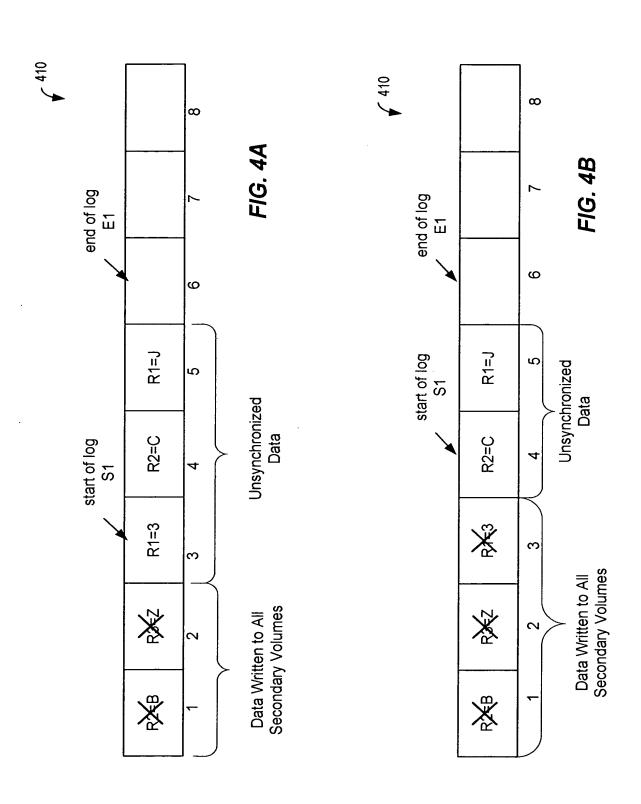




VRT0090P1US Angshuman Bezbaruah

Fast Failover With Multiple Secondary Nodes





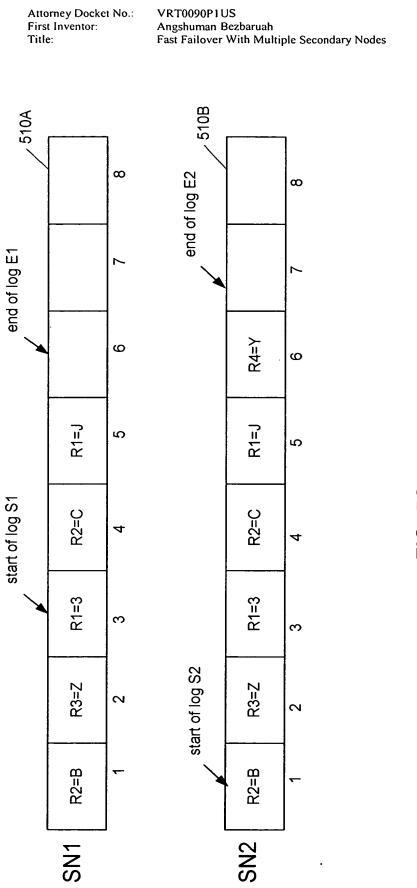


FIG. 5A

VRT0090P1US Angshuman Bezbaruah Fast Failover With Multiple Secondary Nodes end of log E3 510A 510B > 510C ω ω ω end of log E2 R1=8 end of log E1 R4=Y R4=Y 9 9 ဖ start of log S3 R1=J R1=J R1=J 5 S S FIG. 5B start of log S1 R2=C R2=C R2=C 4 4 R1=3 R1=3 R1=3 က က က start of log S2 R3=Z R3=Z R3=Z ~ \sim \sim R2=B R2=B R2=B SN3 SN1 SN₂

Attorney Docket No.: First Inventor:

Title:

